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side by side			
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L20</u>	L17 and l14	5	<u>L20</u>
<u>L19</u>	L17 and l11	32	<u>L19</u>
<u>L18</u>	L17 and l10	104	<u>L18</u>
<u>L17</u>	(vliw or very near1 (large or long)) near35 (updat\$5 or modif\$6 or chan\$5 or miss or hit or compress\$5 or decompress\$5) near15 (field\$1 or portion\$1 or byte\$1 or segment\$1 or section\$1 or word\$1 or bit\$1)	1349	<u>L17</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L16</u>	l13 not l15	3	<u>L16</u>
<u>L15</u>	l9 and l12	11	<u>L15</u>
<u>L14</u>	l9 and l11	5	<u>L14</u>
<u>L13</u>	l9 and l10	14	<u>L13</u>
<u>L12</u>	(712/24)[CCLS]	278	<u>L12</u>

<u>L11</u>	(711/113-221)[CCLS]	25081	<u>L11</u>
<u>L10</u>	(712/2-300)![CCLS]	12218	<u>L10</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L9</u>	L8 not l5	151	<u>L9</u>
<u>L8</u>	L7 near15 (field\$1 or portion\$1 or byte\$1 or segment\$1 or section\$1 or word\$1 or bit\$1)	167	<u>L8</u>
<u>L7</u>	(vliw or very near1 (large or long)) near35 (updat\$5 or modif\$6 or chan\$5 or miss or hit) near15 (which or index\$3 or indices or number)	1594	<u>L7</u>
<u>L6</u>	L5 not l3	17	<u>L6</u>
<u>L5</u>	(vliw or very near1 (large or long)) near15 (updat\$5 or modif\$6 or chan\$5 or miss or hit) near15 (indicat\$3 or detect\$5 or determin\$7) near6 (which or index\$3 or indices or number)	81	<u>L5</u>
<u>L4</u>	L3 not l2	1	<u>L4</u>
<u>L3</u>	(vliw or very near1 (large or long)) near12 (updat\$5 or modif\$6 or chan\$5 or miss or hit) near10 (indicat\$3 or detect\$5 or determin\$7) near6 (which or index\$3 or indices or number)	64	<u>L3</u>
<u>L2</u>	(vliw or very near1 (large or long)) near12 (updat\$5 or modif\$6 or chan\$5) near10 (indicat\$3 or detect\$5 or determin\$7) near6 (which or index\$3 or indices or number)	63	<u>L2</u>
<u>L1</u>	(vliw or very near1 (large or long)) near12 (updat\$5 or modif\$6 or chan\$5) near10 (indicat\$3 or which or index\$3 or indices or number)	1240	<u>L1</u>

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IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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- ☐ 1. **The Impact of Mpack 2**
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[Signal Processing Magazine, IEEE](#)
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- ☐ 2. **Approximating trigonometric functions with the laws of sines and cosines using the logarithm**
 Arnold, M.G.;
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- ☐ 3. **LZW-based code compression for VLIW embedded systems**
 Chang Hong Lin; Yuan Xie; Wolf, W.;
[Design, Automation and Test in Europe Conference and Exhibition, 2004. Proceedings](#)
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- ☐ 4. **A cost-effective implementation of object-based motion estimation**
 Greiner, J.C.; Sethuraman, R.; van Meerbergen, J.; de Haan, G.;
[Signal Processing Systems, 2003. SIPS 2003. IEEE Workshop on](#)
 27-29 Aug. 2003 Page(s):148 - 153
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- ☐ 5. **A high performance VLIW processor for finite field arithmetic**
 Grabbe, C.; Bednara, M.; von zur Gathen; Shokrollahi, J.; Teich, J.;
[Parallel and Distributed Processing Symposium, 2003. Proceedings, International](#)
 22-26 April 2003 Page(s):6 pp.
 Digital Object Identifier 10.1109/IPDPS.2003.1213351
[AbstractPlus](#) | Full Text: [PDF](#)(329 KB) IEEE CNF

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CPUs**

Alippi, C.; Fornaciari, W.; Pozzi, L.; Sami, M.;

[Rapid System Prototyping, 12th International Workshop on, 2001.](#)

25-27 June 2001 Page(s):50 - 56

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Weiss, M.H.; Walther, U.; Fettweis, G.P.;

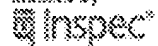
[Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997. IEEE International Conference](#)

Volume 5, 21-24 April 1997 Page(s):4085 - 4088 vol.5

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